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Application Number	09/629,337
Filing Date	August 1, 2000
First Named Inventor	Mark C. Fowler
Art Unit	2672
Examiner Name	Jeffery A. Brier
Attorney Docket Number	0100.0001160

Sheet 1 of 1

NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		PINEDA, JUAN; A Parallel Algorithm for Polygon Rasterization; Computer Graphics; Volume 22, Number 4; August 1988; pages 17-20.	
		AKELEY, KURT; Reality Engine Graphics; Silicon Graphics Computer Systems; Computer Graphics Proceedings; Annual Conference Series; 1993; pages 109-116.	
		VAN HOOK, TIMOTHY J. et al.; Method and Apparatus for Graphics Rasterization; patent application filed December 28, 1999.	

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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 809. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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